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MSLIRF50N50

## MOSFETs Silicon 500V N-Channel MOS

### ■ Applications

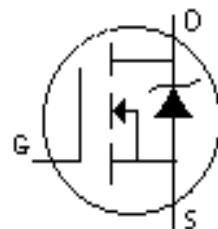
- PWM Inverters
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply (UPS)

### ■ Features

- Low  $R_{DS(ON)}$
- Low Gate Charge
- RoHS Compliant
- 100% UIS and RG Tested

### ■ Product Summary

$V_{DS}$	500	V
$I_D$	50	A
$R_{DS(ON),Typ}@10V$	0.087	$\Omega$
$Q_g$	137	nC



Gate: 1

Drain: 2

Source: 3

TO-247L

Marking	Package	Packaging	Min. package quantity
MSLIRF50N50	TO-247L	Tube	450





## ■ Absolute Maximum Ratings ( $T_c=25^\circ\text{C}$ unless otherwise noted)

Parameter		Symbol	Ratings	Unit
Drain-Source Voltage		$V_{DS}$	500	V
Gate-Source Voltage		$V_{GS}$	$\pm 30$	V
Continuous Drain Current (Note 1)	TC=25°C	$I_D$	50	A
	TC=100°C		25	A
Drain Current-Pulsed (Note 1)		$I_{DM}$	160	A
Total Dissipation		$P_D$	417	W
Junction Temperature		$T_j$	150	°C
Storage Temperature		$T_{stg}$	-55-150	°C
Single Pulse Avalanche Energy (Note 2)		$E_{AS}$	5450	mJ

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

## ■ Thermal Characteristics

Parameter	Symbol	Max	Unit
Maximum Junction-to-Case	$R_{\theta JC}$	0.3	°C/W
Maximum Junction-to-Ambient	$R_{\theta JA}$	40	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}=50\text{V}$ ,  $T_{ch}=25^\circ\text{C}$ (initial),  $L=10\text{mH}$ ,  $R_g=25\Omega$ .

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.





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## ■ Electrical Characteristics (T<sub>c</sub>=25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static Parameters</b>						
Drain-Source Breakdown Voltage	B <sub>V<sub>DSS</sub></sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	500	-	-	V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =500V, V <sub>GS</sub> =0V	-	-	1	uA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V	-	-	±100	nA
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	2	2.7	4	V
Drain-Source On Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =25A	-	0.087	0.1	Ω
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1.0MHz	-	8490	-	pF
Output Capacitance	C <sub>oss</sub>		-	720	-	pF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	40	-	pF
Gate Resistance	R <sub>g</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1.0MHz	-	1.3	-	Ω
<b>Switching Paramters</b>						
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DS</sub> =250V, I <sub>D</sub> =25A, V <sub>GS</sub> =10V, R <sub>G</sub> =25Ω	-	25	-	ns
Turn-On Rise Time	t <sub>r</sub>		-	170	-	ns
Turn-Off Delay Time	t <sub>d(off)</sub>		-	50	-	ns
Turn-Off Fall Time	t <sub>f</sub>		-	70	-	ns
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =400V, I <sub>D</sub> =25A, V <sub>GS</sub> =10V	-	137	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	14	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	26	-	nC
<b>Source-Drain Characteristics</b>						
Max. Diode Forward Cuurent	I <sub>S</sub>		-	-	50	A
Max. Pulsed Forward Cuurent	I <sub>SM</sub>		-	-	160	A
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =25A	-	0.9	1.5	V
Reverse Recovery Time	t <sub>rr</sub>	V <sub>R</sub> =400V, I <sub>F</sub> =30A, di/dt=100A/us	-	470	-	ns
Reverse Recovery Charge	Q <sub>rr</sub>		-	7.7	-	μC

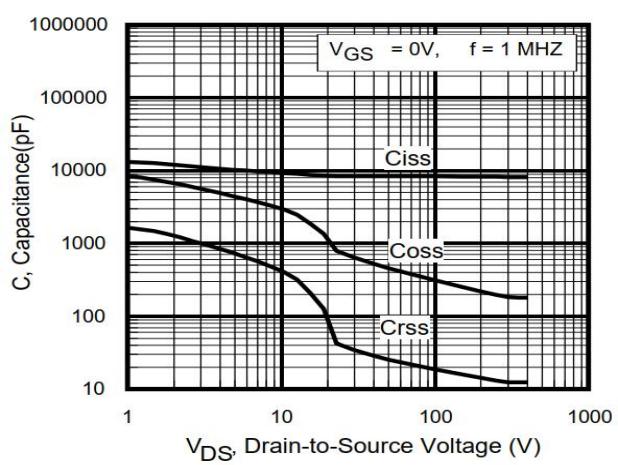
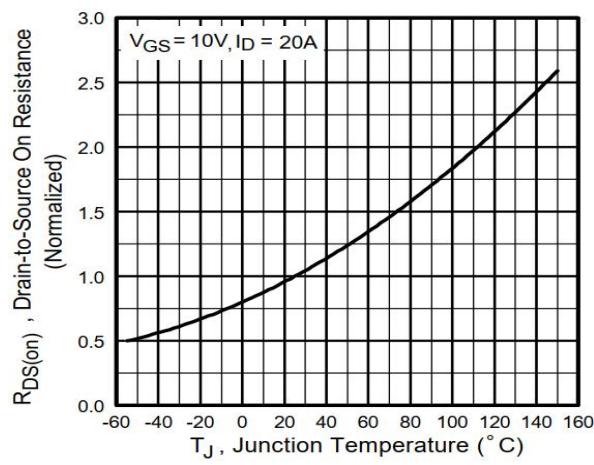
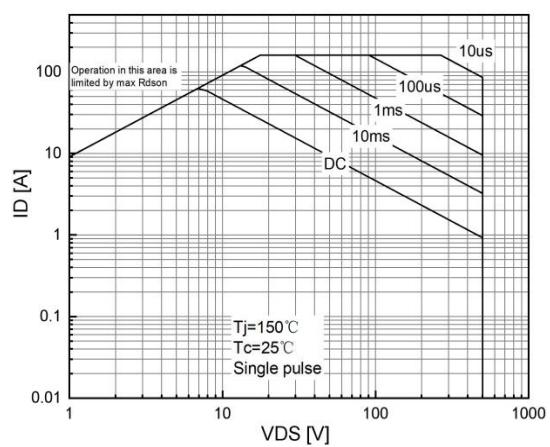
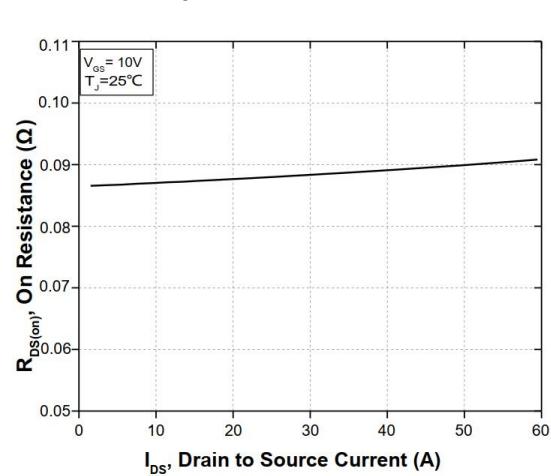
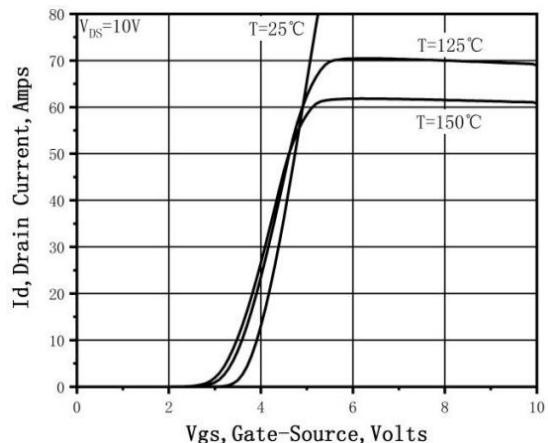
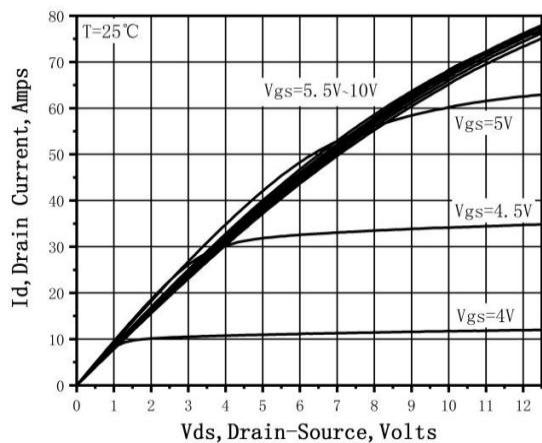




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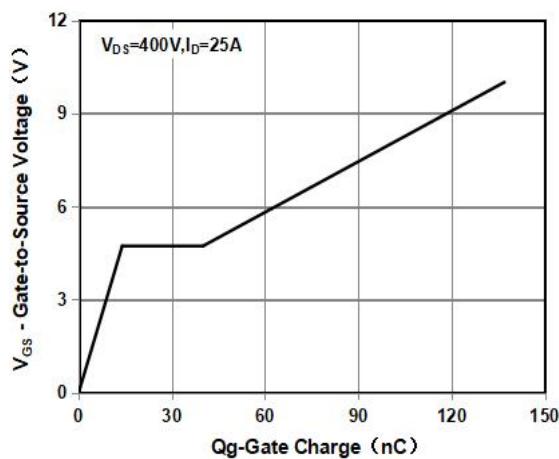
## ■ Characteristics Curves



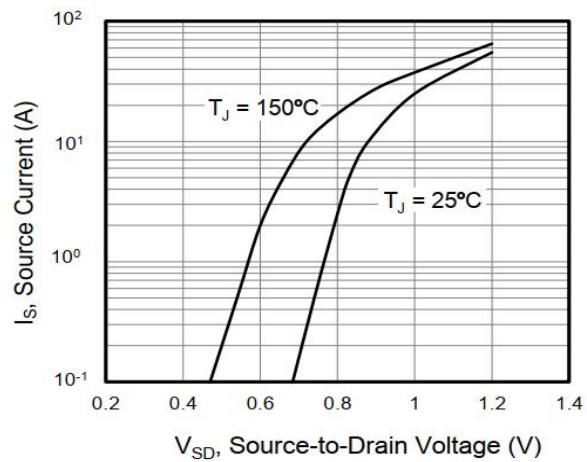


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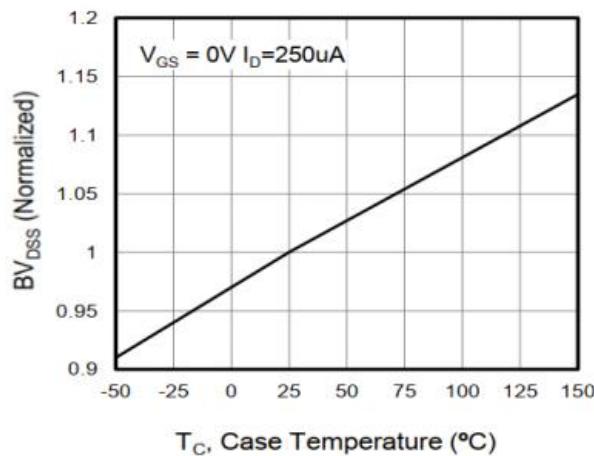
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Gate Charge Waveform



Source-Drain Diode Forward Voltage



Breakdown Voltage Vs Junction Temperature

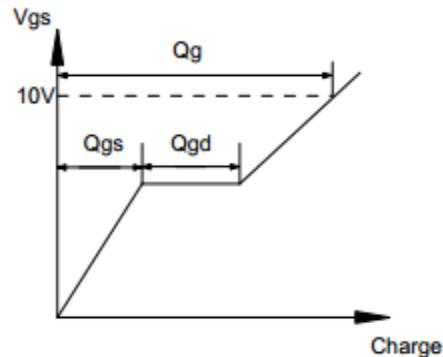
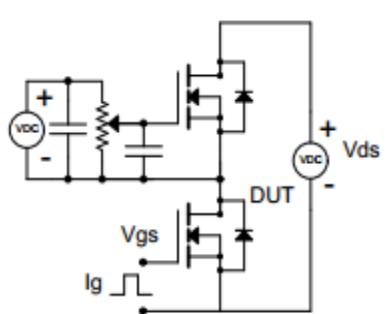
Note : The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



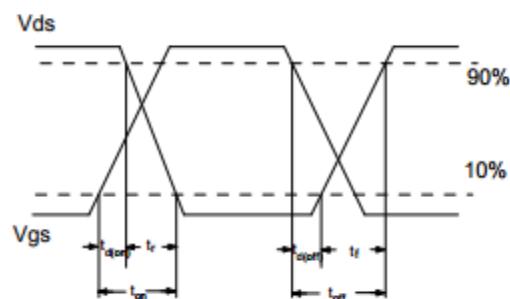
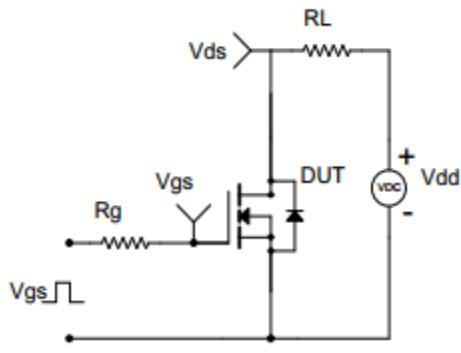


## ■ Test Circuit & Waveform

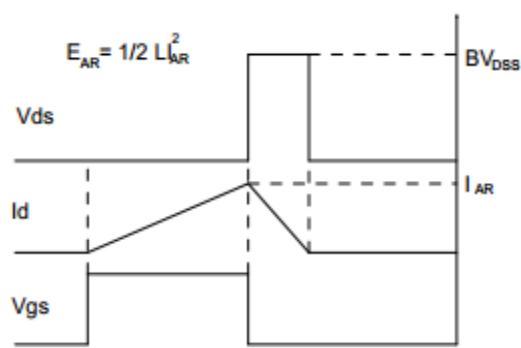
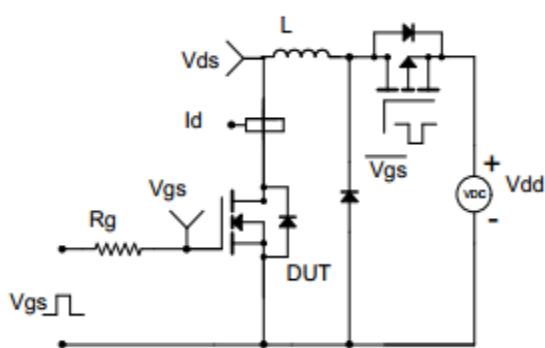
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveform



Unclamped Inductive Switching (UIS) Test Circuit & Waveform





## ■ TO-247L Package Dimensions

Unit: mm

Symbol	Min	Nom	Max	Symbol	Min	Nom	Max
A	4.80		5.20	E1	13.00		13.60
A1	2.20	2.40	2.60	E2	5.00		5.50
A2	1.85		2.15	E3	1.90		2.60
b	1.07		1.33	e		5.44	
b2	1.90		2.16	L	19.30		19.90
b4	2.90		3.20	L1	3.75	3.95	4.15
c	0.52		0.68	ΦP	3.40		3.80
D	20.70		21.30	ΦP1	7.00		7.40
D1	16.15		16.95	S	6.04	6.15	6.30
E	15.50		16.10				

