



## MOSFETs Silicon 60V N-Channel MOS

**■ Applications**

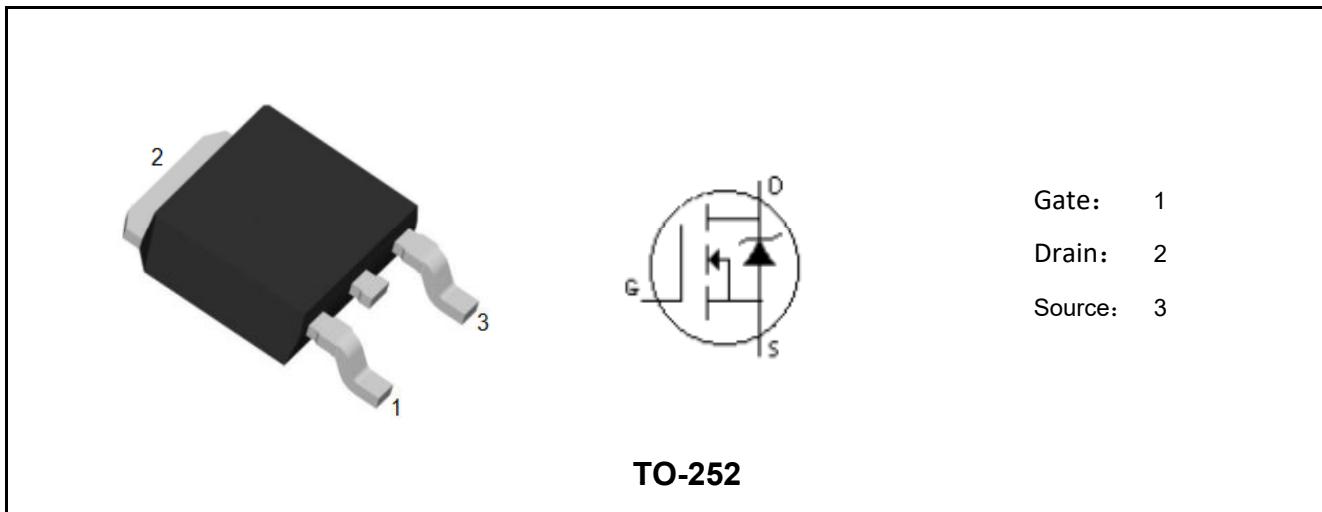
- Synchronous Rectification
- Industrial and Motor Drive
- DC/DC and AC/DC Converters
- Power Tools

**■ Features**

- High-Speed Switching
- Low  $R_{DS(ON)}$
- Capable of 4.5 V Gate Drive
- Good stability and uniformity with high EAS
- RoHS and Halogen-Free Compliant
- 100% UIS and RG Tested

**■ Product Summary**

$V_{DS}$	60	V
$I_D$	25	A
$R_{DS(ON), Typ}@10V$	24	$m\Omega$
$R_{DS(ON), Typ}@4.5V$	30	$m\Omega$
$Q_g$	21	nC



Marking	Package	Packaging	Min. package quantity
MK030R060TL	TO-252	Tape & Reel	3000





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MK030R060TL

## ■ Absolute Maximum Ratings (T<sub>c</sub>=25°C unless otherwise noted)

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V <sub>DS</sub>	60	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current T <sub>c</sub> =25°C (Note 1)	I <sub>D</sub>	25	A
Continuous Drain Current T <sub>c</sub> =100°C (Note 1)		10	A
Drain Current-Pulsed (Note 1)	I <sub>DM</sub>	90	A
Total Dissipation	P <sub>D</sub>	30	W
Junction Temperature	T <sub>j</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-55-150	°C
Single Pulse Avalanche Energy (Note 2)	E <sub>AS</sub>	64	mJ

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

## ■ Thermal Characteristics

Parameter	Symbol	Max	Unit
Maximum Junction-to-Case	R <sub>θJC</sub>	4.36	°C/W
Maximum Junction-to-Ambient (Note 3)	R <sub>θJA</sub>	60	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V<sub>DD</sub>=50V, T<sub>ch</sub>= 25°C(initial), L=0.5mH, R<sub>g</sub>=25Ω.

Note 3: The value of R<sub>θJA</sub> is measured with the device mounted on 1in2 FR-4 board with 2oz. Copper, in a still air environment with T<sub>A</sub>=25° C. The value in any given application depends on the user's specific board design.

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.





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### ■ Electrical Characteristics (Tc=25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static Parameters</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	60	-	-	V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V	-	-	1	uA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.2	1.8	2.5	V
Drain-Source On Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =15A	-	30	38	mΩ
		T <sub>j</sub> =125°C	-	40	-	
		V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	24	30	
		T <sub>j</sub> =125°C	-	34	-	
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =35V, V <sub>GS</sub> =0V, f=1.0MHz	-	1055	-	pF
Output Capacitance	C <sub>oss</sub>		-	59	-	pF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	47	-	pF
Gate Resistance	R <sub>g</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1.0MHz	-	4.24	-	Ω
<b>Switching Paramters</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =30V, I <sub>D</sub> =20A, V <sub>GS</sub> =10V	-	21	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	5.5	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	3.5	-	nC
<b>Source-Drain Characteristics</b>						
Diode Forward Voltage	V <sub>sd</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =10A	-	0.86	1.2	V
Reverse Recovery Time	t <sub>rr</sub>	V <sub>R</sub> =50V, I <sub>F</sub> =20A, di/dt=100A/us	-	43	-	ns
Reverse Recovery Charge	Q <sub>rr</sub>		-	0.04	-	uC

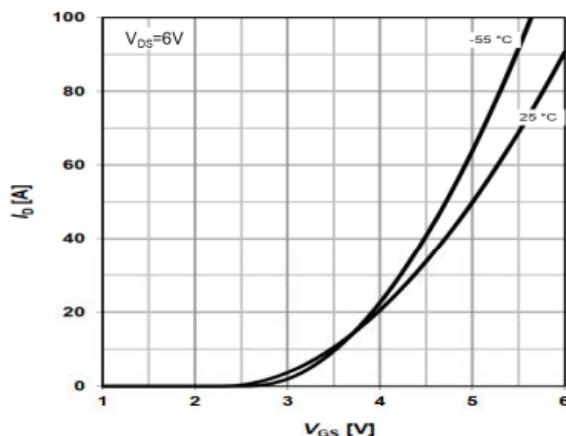
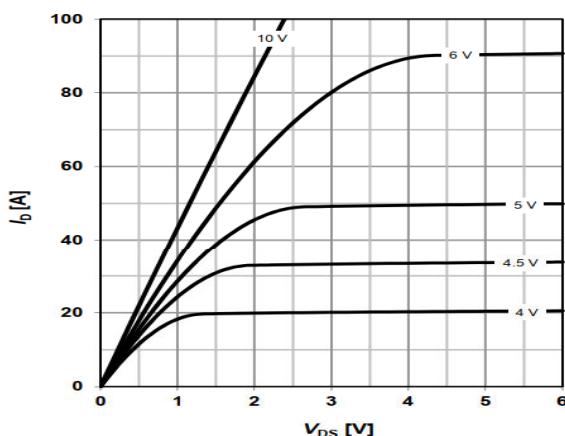




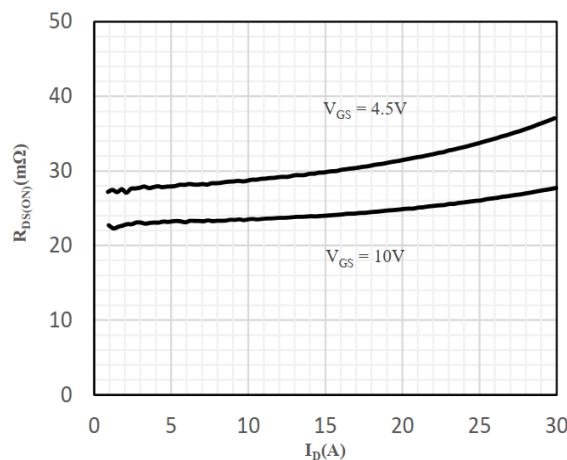
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## ■ Characteristics Curves

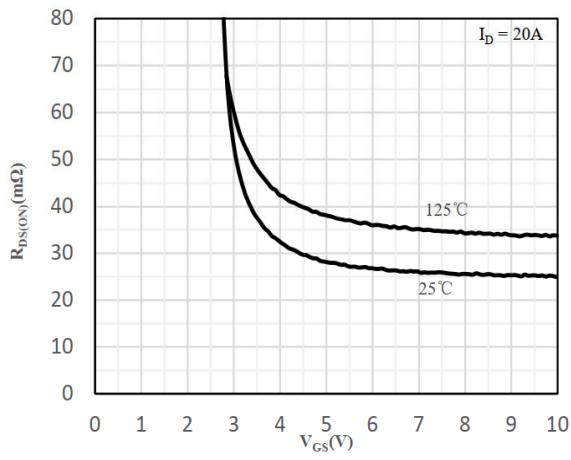


Output Characteristics

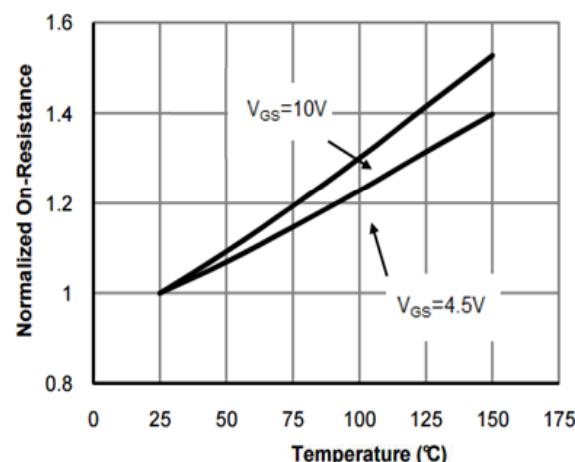


On Resistance Vs Drain Current

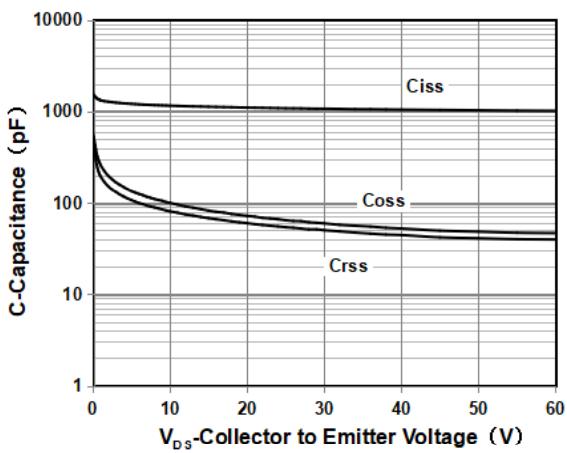
Transfer Characteristics



On Resistance Vs Gate Source Voltage



Rdson-JunctionTemperature



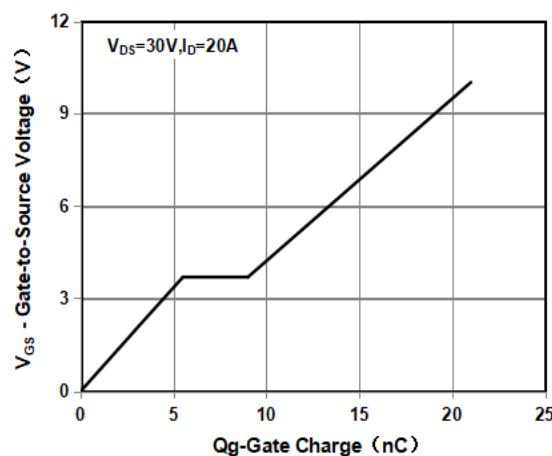
Capacitance



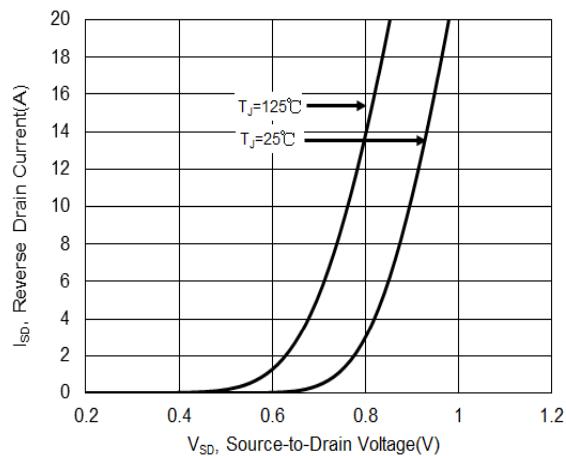


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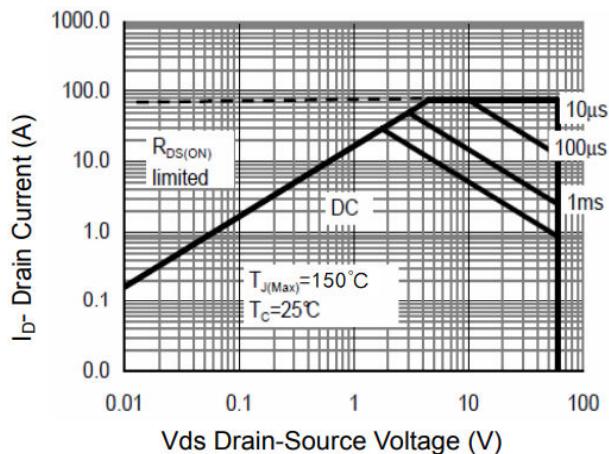
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Gate Charge Waveform



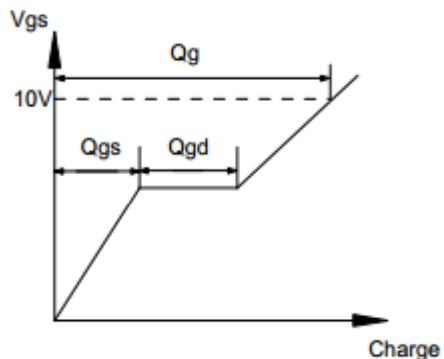
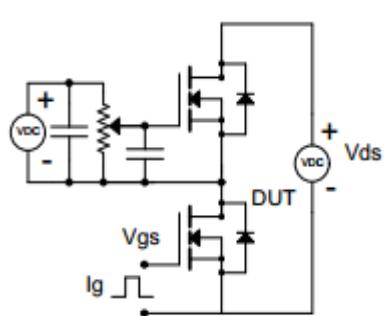
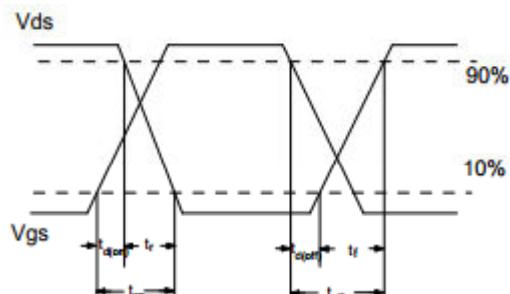
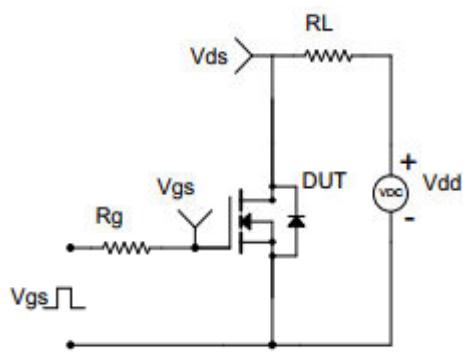
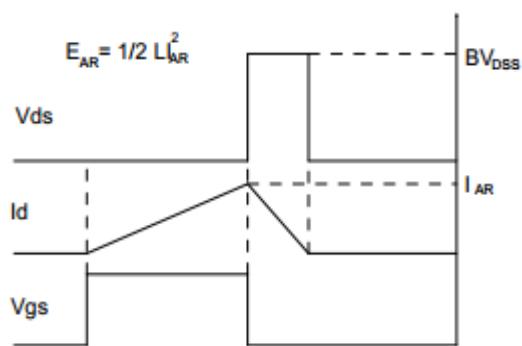
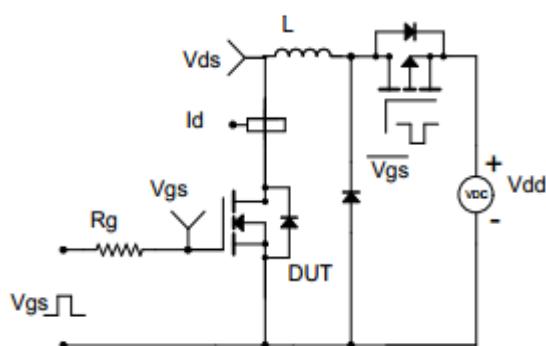
Source-Drain Diode Forward Voltage



Maximum Safe Operating Area

Note : The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



**■ Test Circuit & Waveform****Gate Charge Test Circuit & Waveform****Resistive Switching Test Circuit & Waveform****Unclamped Inductive Switching (UIS) Test Circuit & Waveform**



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## ■ TO-252 Package Dimensions

Unit: mm

Symbol	Min	Nom	Max	Symbol	Min	Nom	Max
A	2.10		2.50	E	5.80		6.30
B	0.80		1.25	e1	2.25	2.30	2.35
b	0.50		0.85	e2	4.45		4.75
b1	0.50		0.90	L1	9.50		10.20
b2	0.45		0.60	L2	0.90		1.45
C	0.45		0.60	L3	0.60		1.10
D	6.35		6.75	K	-0.1		0.10
D1	5.10		5.50				

